MAHARAJA AGRASEN INSTITUTE OF TECHNOLOGY, PSP AREA, SEC-22, ROHINI, DELHI-110086

DATE SHEET FOR REGULAR STUDENTS FOR END-TERM PRACTICAL EXAMS NOV/DEC 2026 (New Marking Scheme w.e.f. 2023 Batch)

B.TECH-FIRST SEMESTER

IST/2ND SHIFT (Only C19,20,21)

Timings : FN - 9:00 AM-11:30AM
N - 11:30 AM-2:00PM
AN - 2:00 PM- 4:30PM

Semester	Course Title	26-Nov-26			27-Nov-26			30-Nov-26			01-Dec-26			02-Dec-26			03-Dec-26			04-Dec-26			05-Dec-26			Lab/Room No.
		FN	N	AN	FN	N	AN	FN	N	AN	FN	N	AN	FN	N	AN	FN	N	AN	FN	N	AN	FN	N	AN	
I	Engineering Physics Lab (ICT-111P) (POOL-A) CSE,IT,ECE	C1	C2	C3	C10	C11	C12	C19	C20	C21							I1	I2	I3	I10	I11	I12	E4	E5	E6	512
		C4	C5	C6	C13	C14	C15										I4	I5	I6	I13	I14	I15				514
		C7	C8	C9	C16	C17	C18										I7	I8	I9	E1	E2	E3				515
I	Applied Physics Lab (BS-151) (POOL-B) CSE-AIML, CST, EEE, ME, EC-VLSI										CST7	CST8	CST9	C(AIML) 1	C(AIML) 2	C(AIML) 3										512
								E(VLSI)1	E(VLSI)2	E(VLSI)3	ME1	ME2	ME3	C(AIML) 4	C(AIML) 5	C(AIML) 6							CST1	CST2	CST3	514
								EE1	EE2	EE3	EE4	EE4/ME 4	ME4	C(AIML) 7	C(AIML) 8	C(AIML) 9							CST4	CST5	CST6	515
I	Engineering Graphics-I Lab (ICT-123P/ES-157) ALL (POOL-A + POOL-B)	C10	C11	C12	C19	C20	C21	CST7	CST8	CST9	C(AIML) 4	C(AIML) 5	C(AIML) 6	I1	I2	I3	I10	I11	I12	E(VLSI)1	E(VLSI)2	E(VLSI)3	C1	C2	C3	312
		C13	C14	C15	CST1	CST2	CST3	ME1	ME2	ME3	C(AIML) 7	C(AIML) 8	C(AIML) 9	I4	I5	I6	I13	I14	I15	E1	E2	E3	C4	C5	C6	227
		C16	C17	C18	CST4	CST5	CST6	EE4	EE4/ME 4	ME4	C(AIML) 1	C(AIML) 2	C(AIML) 3	I7	I8	I9	E4	E5	E6	EE1	EE2	EE3	C7	C8	C9	201
I	Basics of Electrical Engineering Lab (ICT-103P) POOL-A: CSE,IT,ECE	C19	C20	C21	I1	I2	I3	C1	C2	C3	C10	C11	C12	E4	E5	E6										523
		I10	I11	I12	I4	I5	I6	C4	C5	C6	C13	C14	C15	C16	C17	C18										524
		I13	I14	I15	I7	I8	I9	C7	C8	C9	E1	E2	E3													616
I	Programming of Problem Solving Lab (ICT-101P) POOL-A: CSE,IT,ECE										C1	C2	C3	I10	I11	I12	C10	C11	C12	C19	C20	C21	I1	I2	I3	332-A/332-B
											C4	C5	C6	I13	I14	I15	C13	C14	C15	E4	E5	E6	I4	I5	I6	
											C7	C8	C9	E1	E2	E3	C16	C17	C18				I7	I8	I9	
I	Programming in "C" Lab (ES-153) (POOL-B) CSE-AIML, CST, EEE, ME, EC-VLSI	CST7	CST8	CST9	C(AIML) 1	C(AIML) 2	C(AIML) 3	CST1	CST2	CST3																332-A/332-B
		ME1	ME2	ME3	C(AIML) 4	C(AIML) 5	C(AIML) 6	CST4	CST5	CST6																
		E(VLSI)1	E(VLSI)2	E(VLSI)3	EE1	EE2	EE3	C(AIML) 7	C(AIML) 8	C(AIML) 9										EE4	EE4/ME 4	ME4				
I	Environmental Studies Lab (ICT117P/BS-161) ALL (POOL-A +POOL-B)	C(AIML) 1	C(AIML) 2	C(AIML) 3	I10	I11	I12	I1	I2	I3	E(VLSI)1	E(VLSI)2	E(VLSI)3	C19	C20	C21	CST7	CST8	CST9	C1	C2	C3	C10	C11	C12	321-A/321-B
		C(AIML) 4	C(AIML) 5	C(AIML) 6	I13	I14	I15	I4	I5	I6	E4	E5	E6	CST1	CST2	CST3	ME1	ME2	ME3	C4	C5	C6	C13	C14	C15	
		C(AIML) 7	C(AIML) 8	C(AIML) 9	E1	E2	E3	I7	I8	I9	EE1	EE2	EE3	CST4	CST5	CST6	EE4	EE4/ME 4	ME4	C7	C8	C9	C16	C17	C18	
No. of Groups		15	15	15	15	15	15	15	15	15	15	15	15	14	14	14	12	12	12	12	12	12	12	12	12	
Total Students		330	330	330	330	330	330	330	330	330	330	330	330	308	308	308	264	264	264	264	264	264	264	264	264	6468

Branch	Pool	Group Code
CSE-I	A	C 1 to 18
CSE-II	A	C 19 to C21
IT	A	I 1 to 15
ECE	A	E 1 to 6

Branch	Pool	Group Code
CST	B	CST 1 to 9
CSE-AIML	B	C(AIML) 1 to 9
ECE (VLSI)	B	E(VLSI) 1 to 3
EEE		EE 1 to 4

Branch	Pool	Group Code
ME	B	ME 1 to 4

Dated: 27.10.2026

Director/Addl. Director