

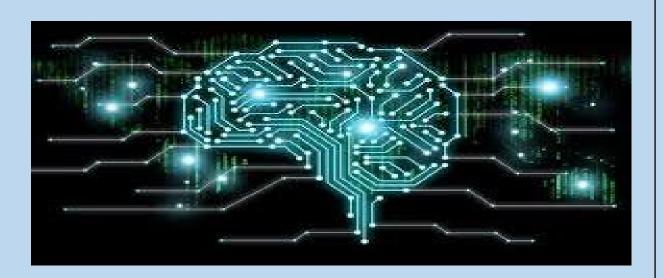
Maharaja Agrasen Institute of Technology, Delhi Department of Electrical and Electronics Engineering

PRESENTS

Faculty Development Program (FDP)

on

"Latest Technologies and Their Applications in Electrical Engineering and Future Energy" 16 – 20 August, 2021 (Online mode)



About the FDP

This online Faculty Development Program (FDP) on "Latest Technologies and Their Applications in Electrical Engineering and Future Energy" aims at introducing the undergraduate engineering faculty, industry personnel and research scholars with the latest developments in the field of Electrical Engineering and Future Energy. The hands on sessions with experts will give insight as to how the theory is transformed to reality for end user to reap the benefit of technology

Contact Us: 9999985209 - Prof.(Dr.) Rajveer Mittal, HoD (EEE),(Convener)

9818006038 - Dr. Monika Gupta (Coordinator) 9811893230 - Dr. Neelu Nagpal (Coordinator) 8851424257 - Mr. Ashok Goyal (Coordinator)





MEET OUR DIGNITARIES



Dr. Nand Kishore GargChancellor, MAU, Himachal Pradesh &
Founder and Chief Advisor, MATES

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Director, MAIT



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Prof. (Dr.) M.L. GoyalVice Chairman, Academics



Prof. (Dr.) S.S. Deswal Dean, Academics

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Chief Guest (Inauguration)



Prof. (Dr.) A P Mittal
(Ex Member Secretary, AICTE & Director West Campus, NSUT)

Invited Speakers



Prof. A K Tandon
Founder Director BPIT



Prof. Smriti Srivastava NSUT, Delhi



Prof. Vijayant Agarwal (NSUT)
NSUT, Delhi.



Sh. Rajesh Shukla
Depty Director, NPTI
Badarpur



Dr. Lakshmi D VIT, Bhopal



Dr. Saurabh Bhardwaj TIET, Patalia



Dr . Rajesh Kumar DTU



Mr. Manish Vohra Consultant



Dr Om Prakash Jena Ravenshaw Univ.



MAHARAJA AGARSEN INSTITUTE OF TECHNOLOGY

Maharaja Agrasen Institute of Technology, Delhi is affiliated to Guru Gobind Singh Indraprastha University, Delhi, India (NBA Accredited, ISO 9000:2015 Certified, Graded as 'A' by the National Assessment and Accreditation Council, Bangalore). It was established in the year 1999 and since then it has taken long strides in providing quality Engineering and Technical education to students. Keeping in view the values ushered in by Maharaja Agrasen and the objectives envisioned by the Maharaja Agrasen Technical Education Society, the Institute aims to provide well qualified manpower to this nation.

ABOUT EEE DEPARTMENT

The Department of Electrical and Electronics Engineering has flourished from intake of 60 students in 2004 to present intake of 180 students per year under guidance of Prof. (Dr.) Rajveer Mittal. Vision of department is to produce technically competent human resource for electrical and electronics industry with high moral and ethical values. It is an immense pleasure to find alumni of the department getting placed in government jobs and almost all private and multinational companies. The follow-up of the university curriculum, blending core electrical subjects like machines, control and power systems with those of electronics-based communication, VLSI design and



Prof. (Dr.) Rajveer Mittal HOD (EEE).

microcontrollers have helped n enriching the broad knowledge based with cutting edge technology to foster self-development and confidence to do good & prove one's own worth. The department is equipped with facilities like well-established laboratories, rich library, seminar hall, and AC classrooms and staff rooms. The department conducts lot of quality activities every year to update the knowledge of students and faculty. The inherent skills of our students are being well nurtured by highly qualified faculty and hardworking staff in achieving goals & objectives of the Department.

Participants

Academicians, Research Scholars, Industry People

Certification

All the participants will be provided with an E-certificate of Participation after successful completion of FDP.

Registration

Registration Fees:

For Academicians, Research Scholars and people from Industry Rs. 500/-

Registration Link:

https://forms.gle/Ys6Zn4wa1VJmkjCK6

or scan here to register



Payment to be made in favour of

Account Holder's	Maharaja Agrasen
Name	Institute of Technology
Bank Name	Union Bank of India
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Founder & Chief Advisor, MATES Chancellor, Maharaja Agrasen University (HP)

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Sh. S.P. Aggarwal

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Sh. Manoj Singhal

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Patrons

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MAIT

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MBA)

Prof. (Dr.) V. N. Mathur

Dr. Vaibhav Jain (HOD –

(HOD - MAE)

ME)

Prof. (Dr.) Chandra Prakash

(HOD – Applied Sciences)

For queries please contact: -

Dr. Monika Gupta : 9818006038 Dr. Neelu Nagpal : 9811893230 Mr. Ashok Goyal : 8851424257



Maharaja Agrasen Institute of Technology

(Approved by AICTE and Affiliated to GGSIP University)

One Week Faculty Development Program cum Workshop

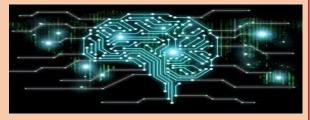
on

"Latest Technologies and their Applications in Electrical Engineering and Future Energy"

August 16-20, 2021 (Online Mode)

Organized by

Department of Electrical and Electronics Engineering (EEE)



Convener

Prof. (Dr.) Rajveer Mittal, HOD (EEE)

Coordinators

Dr. Monika Dr. Neelu Mr. Ashok Gupta Nagpal Goyal

Maharaja Agrasen Institute of Technology

Plot No. 1, Sector – 22, Rohini, Delhi-110086 Ph.011-27582095, 65151162/63, 65162001 Website: www.mait.ac.in, E-mail: mait@mait.ac.in

Maharaja Agrasen Institute of Technology

Maharaja Agrasen Institute of Technology was established in 1999 by Maharaja Agrasen Technical Education Society promoted by a group of well-known Industrialists, Businessmen, Professionals and Philanthropists with an aim to promote quality education in the field of Technology and Management. Since then, MAIT has grown from strength to strength to emerge as one of the top technical institutes among the private Institutes and has been constantly ranked amongst the top engineering Institutes by DATAQUEST.

To build lasting relations with Industry, MAIT joins hands with industry in fulfilling the social responsibility of imparting industry relevant technical education and training. Its Alumni have also distinguished themselves through their achievements in industry, academics, research, business, government and social domains. The institute continues to work closely with the Alumni to enhance its activities through interactions in academic and research programme.

Department of Electrical and Electronics Engineering (EEE)

The Department of Electrical and Electronics Engineering has flourished from intake of 60 students in 2004 to present intake of 180 students per year. Vision of department is to produce technically competent human resource for electrical and electronics industry with high moral and ethical values. It is an immense pleasure to find alumni of the department getting placed in government jobs and almost all private and multinational companies. The follow-up of the university curriculum, blending core electrical subjects like machines, control and power systems with those of electronics-based communication, VLSI design and microcontrollers have helped n enriching the broad knowledge based with cutting edge technology to foster self-development and confidence to do good & prove one's own worth. The department is equipped with facilities like well-established laboratories, rich library, seminar hall, and AC classrooms and staff rooms. The department conducts lot of quality activities every year to update the knowledge of students and faculty. The inherent skills of our students are being well nurtured by highly qualified faculty and hardworking staff in achieving goals & objectives of the Department.

About the FDP

The FDP will be focussing on giving theoretical and practical experience on advanced technologies like LABVIEW, Deep neural network, Machine Learning. The LabVIEW is a software that is used in industries. It provides the efficient SCADA system and a real time control. It is also useful for research scholars as MATLAB can run within LabVIEW and a hardware interface can be implemented.

Neural Networks, Deep Neural Networks technology is aimed at solving problems such as pattern recognition, speech recognition, character recognition which are difficult for traditional computational methods. Neural networks have potential applications in many industrial areas such as advanced robotics, operations research, and process engineering.

Machine Learning is used for Image recognition, translating speech into text, medical diagnosis.

Objective of FDP

After attending the FDP, the participants would be able to:

- Understand the fundamentals of recent advanced technologies like LABVIEW, Machine Learning, Optimization Techniques
- Use these technologies to solve many industrial and research problems such as pattern recognition, medical diagnosis and many more.

Expected Outcomes

- Learn the fundamentals of programming in AI
- Get hands on experience on various tools and interfacing techniques in LabVIEW.
- Get acquitted with advancement in Optimization techniques, Neural Networks, Deep Neural Networks.
- Application of AI in future energy
- Familiarize with the fundamentals of machine learning and 'Phython' programming

Chief Guest (Innaugration)

Prof. (Dr.) A P Mittal (Ex Member Secretary, AICTE & Director West Campus, NSUT)

Eminent Resource Persons

Prof. A. K. Tandon (Founder Director BPIT)

Prof. (Dr.) Smriti Srivastava (NSUT, New Delhi)

Prof. (Dr.) Vijayant Aggarwal (NSUT, New Delhi)

Sh. Rajesh Shukla (Depty Director, NPTI, Badarpur)

Dr. Lakshmi D (VIT, Bhopal)

Dr. Saurabh Bhardwaj (TIET, Patiala)

Dr. Rajesh Kumar (DTU, New Delhi)

Mr. Manish Vohra (Consultant)

Dr. Om Prakash Jena (Ravenshaw University, Odisha)

Members

Mr. U.k.Jha

Dr. L.P.Singh

Mr. S.k Pandey

Dr. Neelam Kesarwani

Mr. Jitender Kumar

Mr. Sheersh Garg

Ms. Supriya Sharma

Ms. Poonam Juneja

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Ms. Monika Bhardwaj

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Mr. Rahul Garg

Ms. Jyoti Gupta

Dr. Laxya

Mr. Chanderkesh Shukla



Maharaja Agrasen Institute of Technology, Delhi Department of Electrical and Electronics Engineering Faculty Development Program

"Latest Technologies and Their Applications in Electrical Engineering and Future Energy" (16-20August, 2021)

Program schedule (online mode)

	10:00 am-11:00 am	11:00pm-1:00pm	Lunch 1:00pm-	2:00 pm-4:00pm
Day 1	Inauguration	11:00-12:00	02:00	Workshop on Python
(16 Aug)	Chief Guest: Prof. A. P. Mittal, Ex Member Secretary, AICTE & Director West Campus, NSUT	Machine Learning at Glance Dr. Om Prakash Jena, Ravenshaw University, Odisha. 12:00 – 1:00 PM: Industrial Applications of AI Prof. Vijayant Agarwal, NSUT	pm	Dr. Lakshmi D, VIT, Bhopal
	10:30 am to 1:00 pm			2:00 pm-4:00pm
Day 2	Introduction to LABVIEW			Introduction to LABVIEW
(17 Aug)	Mr. Manish Vohra (Trainer/Consultant)			Mr. Manish Vohra (Trainer/ Consultant)
Day 3	Industrial and research application of LABVIEW			Activity Based Lecture an Important
(18 Aug)	Mr. Manish Vohra			Component of Technical Education
	(Trainer/ Consultant)			Prof A k Tandon (Founder Director BPIT)
Day 4	Optimisation Techniques			Artificial neural network and their Applications
(19 Aug)	Prof. Smriti Srivastava (NSUT)			Dr Rajesh Kumar (DTU)
	10:00 am-11:00	11:00pm-1:00pm		2:00 pm-3:00pm
	am			
Day 5 (20 Aug)	Future Energy Sh. Rajesh Shukla (Depty Director, NPTI)	Deep Neural Network Dr Saurabh Bhardwaj (TIET, Patiala)		Valedictory Session

Prof. (Dr.) Rajveer Mittal (HOD, EEE) (FDP Convener)

Dr. Monika Gupta, Dr Neelu Nagpal, Mr Ashok Goyal (FDP coordinators)

Maharaja Agrasen Institute of Technology

MAIT/Notice/2021 16/08/2021

Notice

Department of Electrical & Electronics Engineering (EEE), MAIT, is organizing One Week Faculty Development Program cum Workshop on "Latest Technologies and Their Applications in Electrical Engineering and Future Energy" from 16/08/2021 to 20/08/2021. The inaugural schedule is as follows:

Program (16/08/2021)	Speaker	Time
10:15-11:00 am		
Saraswati Vandana	Online	10:15-10:20 am
Address by the Director, MAIT	Prof. (Dr.) Neelam Sharma	10:20-10:25 am
Briefing of FDP by Coordinators	Dr Monika Gupta, Dr Neelu Nagpal, Mr. Ashok Goyal	10:25-10:30 am
Address by the Dean, MAIT	Prof. (Dr.) S.S. Deswal	10:30-10:35am
Address by Chief Guest (Ex. Member Secretary, Director, West Campus, NSUT)	Prof (Dr.) A.P. Mittal	10:35-10:45am
Address by Founder and Chief Advisor, MATES	Dr. Nand Kishore Garg	10:45-10:55 am
Vote of Thanks by the HoD, EEE	Prof. (Dr.) Rajveer Mittal	10:55-11:00 am

All registered participants are invited for the inaugural session at 10 AM on 16th August 2021.

MST Link for all sessions: https://bit.ly/2VGJwmZ

Prof. (Dr.) Neelam Sharma

Director

MAIT Website CC to:

- 1. Founder and Chief Advisor
- 2. Chairman
- 3. Executive Chairman
- 4. General Secretary
- 5. Chief Executive
- 6. Vice Chairman
- 7. Secretary
- 8. Director (MAIMS)
- 9. Dean Academics
- 10. All HoD's
- 11. All Faculty members of MAIT and MAIMS