

**MAHARAJA AGRASEN INSTITUTE OF TECHNOLOGY, PSP AREA, SEC-22, ROHINI, DELHI-110086**  
**DATE SHEET FOR REGULAR STUDENTS FOR END-TERM PRACTICAL EXAMS MAY 2026 (New Marking Scheme w.e.f. 2023 Batch)**

Timings : FN - 9:00 AM-11:30AM  
 N - 11:30 AM-2:00PM  
 AN - 2:00 PM- 4:30PM

**B.TECH-SECOND SEMESTER**  
**IST/2ND SHIFT (Only C19,20,21)**

<b>II SEMESTER</b>																									
Course Title	07-May-26			08-May-26			11-May-26			12-May-26			13-May-26			14-May-26			15-May-26			18-May-26			Lab/Room No.
	FN	N	AN	FN	N	AN	FN	N	AN	FN	N	AN	FN	N	AN	FN	N	AN	FN	N	AN	FN	N	AN	
Physics -II Lab (BS-152) ALL (POOL-A + POOL-B)	C1	C2	C3	C10	C11	C12	C19	C20	C21	CST7	CST8	CST9	C(AI)1	C(AI)2	C(AI)3	I1	I2	I3	I10	I11	I12	E(VLSI)1	E(VLSI)2	E(VLSI)3	512
	C4	C5	C6	C13	C14	C15	CST1	CST2	CST3	ME1	ME2	ME3	C(DS)1	C(DS)2	C(DS)3	I4	I5	I6	I13	I14	I15	E(ACT)1	E(ACT)2	E(ACT)3	514
	C7	C8	C9	C16	C17	C18	CST4	CST5	CST6				C(AIML)1	C(AIML)2	C(AIML)3	I7	I8	I9	E1	E2	E3	EE1	EE2	EE3	515
Engineering Graphics-II Lab (ES-158) ALL (POOL-A + POOL-B)	C10	C11	C12	C19	C20	C21	CST7	CST8	CST9	C(AI)1	C(AI)2	C(AI)3	I1	I2	I3	I10	I11	I12	E(VLSI)1	E(VLSI)2	E(VLSI)3	C1	C2	C3	235
	C13	C14	C15	CST1	CST2	CST3	ME1	ME2	ME3	C(DS)1	C(DS)2	C(DS)3	I4	I5	I6	I13	I14	I15	E(ACT)1	E(ACT)2	E(ACT)3	C4	C5	C6	236
	C16	C17	C18	CST4	CST5	CST6				C(AIML)1	C(AIML)2	C(AIML)3	I7	I8	I9	E1	E2	E3	EE1	EE2	EE3	C7	C8	C9	212
Workshop Practice Lab (ES-164) ALL (POOL-A + POOL-B)	C19	C20	C21	CST7	CST8	CST9	C(AI)1	C(AI)2	C(AI)3	I1	I2	I3	I10	I11	I12	E(VLSI)1	E(VLSI)2	E(VLSI)3	C1	C2	C3	C10	C11	C12	211
	CST1	CST2	CST3	ME1	ME2	ME3	C(DS)1	C(DS)2	C(DS)3	I4	I5	I6	I13	I14	I15	E(ACT)1	E(ACT)2	E(ACT)3	C4	C5	C6	C13	C14	C15	201
	CST4	CST5	CST6				C(AIML)1	C(AIML)2	C(AIML)3	I7	I8	I9	E1	E2	E3	EE1	EE2	EE3	C7	C8	C9	C16	C17	C18	231
Applied Chemistry Lab (BS-156) POOL-B: C(AI)/C(DS)/C(AIML)/IT/ITE/ECE/EC-VLSI/EC-ACT/EEE													E(VLSI)1	E(VLSI)2	E(VLSI)3	C(AI)1	C(AI)2	C(AI)3	I1	I2	I3	I10	I11	I12	321-A/321-B
													E(ACT)1	E(ACT)2	E(ACT)3	C(DS)1	C(DS)2	C(DS)3	I4	I5	I6	I13	I14	I15	
													EE1	EE2	EE3	C(AIML)1	C(AIML)2	C(AIML)3	I7	I8	I9	E1	E2	E3	
Electrical Science Lab (ES-160) POOL-B: C(AI)/C(DS)/C(AIML)/IT/ITE/ECE/EC-VLSI/EC-ACT/EEE	C(AI)1	C(AI)2	C(AI)3	I1	I2	I3	I10	I11	I12	E(VLSI)1	E(VLSI)2	E(VLSI)3													523
	C(DS)1	C(DS)2	C(DS)3	I4	I5	I6	I13	I14	I15	E(ACT)1	E(ACT)2	E(ACT)3													524
	C(AIML)1	C(AIML)2	C(AIML)3	I7	I8	I9	E1	E2	E3	EE1	EE2	EE3													615
Programming in "C" Lab (ES-154) POOL-A: CSE/CSE-II/CST/ME													C1	C2	C3	C10	C11	C12	C19	C20	C21	CST7	CST8	CST9	332-A/332-B
													C4	C5	C6	C13	C14	C15	CST1	CST2	CST3	ME1	ME2	ME3	
													C7	C8	C9	C16	C17	C18	CST4	CST5	CST6				
Environmental Studies Lab (BS-162) POOL-A: CSE/CSE-II/CST/ME	CST7	CST8	CST9	C1	C2	C3	C10	C11	C12	C19	C20	C21													321-A/321-B
	ME1	ME2	ME3	C4	C5	C6	C13	C14	C15	CST1	CST2	CST3													
				C7	C8	C9	C16	C17	C18	CST4	CST5	CST6													

Branch	Pool	Group Code	Branch	Pool	Group Code	Branch	Pool	Group Code
CSE-I	A	C 1 to 18	CSE-AI	B	C(AI) 1 to 3	ECE	B	E 1 to 3
CSE-II	A	C 19 to C21	CSE-DS	B	C(DS) 1 to 3	ECE (VLSI)	B	E(VLSI) 1 to 3
CST	A	CST 1 to 9	CSE-AIML	B	C(AIML) 1 to 3	ECE (ACT)	B	E(ACT) 1To 3
ME	A	M 1 to 3	IT	B	I 1 to 15	EEE	B	EE 1 To 3

Dated : 16.04.2026

Dean

Director